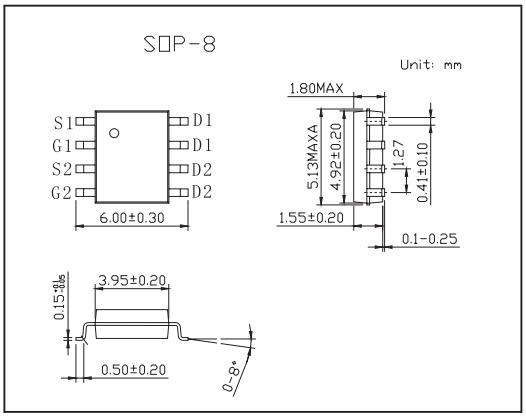
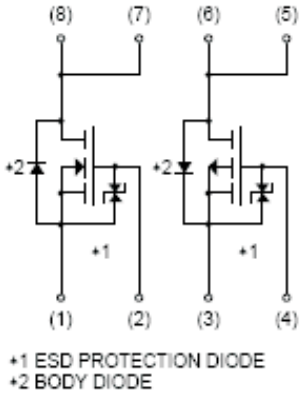
Switching

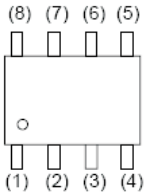
KP8M7

■ Features

- Low on-resistance.
- Built-in G-S Protection Diode.
- Small and Surface Mount Package.
- Power switching, DC / DC converter.







■ Absolute Maximum Ratings Ta = 25℃

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-source voltage	V_{DSS}	30	-30	V
Gate-source voltage	V_{GSS}	±20	±20	V
Drain current Continuous	I_D	±5.0	±7.0	A
Drain current Pulsed *	I_{DP}	±20	±28	A
Source current (Body diode) Continuous	I_S	1.6	-1.6	A
Source current (Body diode) Pulsed *	I_{SP}	6.4	-28	A
Total power dissipation	P_D	2		W
Channel temperature	T_{ch}	150		℃
Storage temperature	T_{stg}	-55 to +150		℃
Channel to ambient	$R_{th\ (ch-a)}$	62.5		℃/W

* Pw≤10μs, Duty cycle≤1%

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■ Electrical Characteristics Ta = 25℃

Parameter	Symbol	Testconditons		Min	Typ	Max	Unit
Gate-source leakage	I_{GSS}	V_{GS}=±20V,V_{DS}=0V	N-Ch			±10	μA
		V_{GS}=±20V,V_{DS}=0V	P-Ch			±10	
Drain-source breakdown voltage	$V_{(BR)\ DSS}$	I_D=1mA,V_{GS}=0V	N-Ch	30			V
		I_D=-1mA,V_{GS}=0V	P-Ch	-30			
Zero gate voltage drain current	I_{DSS}	V_{DS}=30V,V_{GS}=0V	N-Ch			1	μA
		V_{DS}=-30V,V_{GS}=0V	P-Ch			-1	
Gate threshold voltage	$V_{GS\ (th)}$	V_{DS}=10V,I_D=1mA	N-Ch	1.0		2.5	V
		V_{DS}=-10V,I_D=-1mA	P-Ch	-1.0		-2.5	
Static drain-source on-state resistance	$R_{DS\ (on)}$	I_D=5.0A,V_{GS}=10A	N-Ch		36	51	mΩ
		I_D=5.0A,V_{GS}=4.5V			52	73	
		I_D=5.0A,V_{GS}=4V			58	82	
Static drain-source on-state resistance	$R_{DS\ (on)}$	I_D=-7A,V_{GS}=-10A	P-Ch		20	28	mΩ
		I_D=-7A,V_{GS}=-4.5V			25	35	
		I_D=-7A,V_{GS}=-4.0V			30	42	
Forward transfer admittance	$\|Y_{fs}\|$	I_D=5.0A,V_{DS}=10V	N-Ch	3.0			S
		I_D=-7A,V_{DS}=-10V	P-Ch	6.0			
Input capacitance	C_{iss}	N-Channel V_{DS}=10V,V_{GS}=0V,f=1MHz	N-Ch		230		pF
			P-Ch		2600		
Output capacitance	C_{oss}		N-Ch		80		pF
		P-Channel V_{DS}=-10V,V_{GS}=0V,f=1MHz	P-Ch		450		
Reverse transfer capacitance	C_{rss}		N-Ch		50		pF
			P-Ch		350		
Turn-on delay time	$t_{d\ (on)}$	I_D=2.5A,V_{DD}=15V	N-Ch		6		ns
		I_D=-3.5A,V_{DD}=-15V	P-Ch		20		
Rise time	t_r	N-Channel V_{GS}=10V,R_L=6.0Ω,R_G=10Ω	N-Ch		8		ns
			P-Ch		50		
Turn-off delay time	$t_{d\ (off)}$	P-Channel V_{GS}=-10V,R_L=4.3Ω,R_G=10Ω	N-Ch		22		ns
			P-Ch		110		
Fall time	t_f		N-Ch		5		ns
			P-Ch		70		
Total gate charge	Q_g	N-Channel V_{DD}=15V,V_{GS}=5V,I_D=5.0A	N-Ch		3.9	5.5	nC
			P-Ch		25		
Gate-source charge	Q_{gs}	P-Channel V_{DD}=-15V,V_{GS}=-5V,I_D=-7.0A	N-Ch		1.1		nC
			P-Ch		5.5		
Gate-drain charge	Q_{gd}		N-Ch		1.4		nC
			P-Ch		10		
Forward voltage	V_{SD}	I_S=6.4A, V_{GS}=0V	N-Ch			1.2	V
		I_S=-1.6A, V_{GS}=0V	P-Ch			-1.2	